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ABSTRACT OF THE DISCLOSURE

A single unified digital subscriber line (DSL) transceiver can be used for DSL applications, can be integrated on a single board or chip, or within a less board space. An embodiment of the unified DSL transceiver comprises a transmit circuit coupled to a line driver/transformer circuit. The line driver/transformer circuit is coupled to a receive circuit that comprises a switching circuit. A signal is transmitted from the transmit circuit to the line driver/transformer circuit; the switching circuit is configured based on DSL applications, and the signal is propagated from the line driver/transformer circuit to a line.